

**Amendments to the Claims**

1. (CURRENTLY AMENDED) An active matrix liquid crystal display device having an array of picture elements-(12), each comprising a picture element electrode (14)-and a switching device-(16), located at respective intersections between crossing sets of selection and data address conductors (18, 20)-connected to the picture elements, and a set of connection lines (30) for supplying selection signals to the set of selection address conductors-(18), which connection lines extend from one side of the array in the direction of the set of data address conductors (20)-and are connected to respective ones of the set of selection address conductors-(18), wherein each picture element includes a storage capacitor (22)-connected between the picture element electrode and a capacitor line (40)-shared by the picture elements in the same row, and wherein the selection address conductor associated with one row of picture elements is coupled to the capacitor line associated with a different row of picture elements so that each connection line is connected to a respective selection address conductor for one row of picture elements and its coupled capacitor line for another row of picture elements.
2. (CURRENTLY AMENDED) A device according to Claim 1, wherein the selection address conductor (18)-associated with one row of picture elements is coupled to the capacitor line (40)-associated with an adjacent row of picture elements.
3. (CURRENTLY AMENDED) A device according to Claim 1-or-Claim 2, wherein a selection address conductor and a capacitor line (40)-are coupled by an interconnection (45)-between their ends at one side of the array.
4. (ORIGINAL) A device according to Claim 3, wherein the interconnections for successive selection address conductors and their respective associated capacitor lines are arranged alternately at opposite sides of the array.
5. (CURRENTLY AMENDED) A device according to any one of the preceding claims, wherein each connection line (30)-extends from one side of the array and is connected at a connection point (32)-to the selection address conductor

(18) or the capacitor line (40) with which it is associated that is closest to that side, and wherein the connection line (30) terminates at that connection point.

6. (CURRENTLY AMENDED) A device according to any one of the preceding claims, wherein the capacitor line ~~(40)~~ and selection address conductor associated with one row of picture elements extend along opposite sides of the row of picture elements.

7. (CURRENTLY AMENDED) A device according to any one of the preceding claims, wherein the picture element array is driven using a capacitively coupled drive scheme in which part of the drive voltage applied to the picture element electrode is provided via the storage capacitor ~~(22)~~.